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Patent
Attorney's Docket No. H1442

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
)	
Wiley Eugene HILL et al.)	Group Art Unit: 2818
)	
Application No.: 10/728,910)	Examiner: Tu Tu V. Ho
)	
Filed: December 8, 2003)	
)	
For: SRAM FORMATION USING)	
SHADOW IMPLANTATION)	
)	

INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

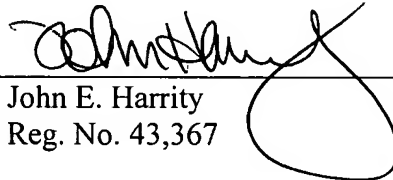
Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of the IDS is required.
- ☐ The fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ Charge \$ _ to Deposit Account No. 50-1070 for the fee due.
- ☐ A check in the amount of \$ _ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any other appropriate fees that may be required by this paper that are not accounted for above, and to credit any overpayment, to Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By: 
John E. Harrity
Reg. No. 43,367

11240 Waples Mill Road
Suite 300
Fairfax, Virginia 22030
(571) 432-0800

CUSTOMER NUMBER: 45114

Date: July 1, 2005



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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(i)

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401 Dulany Street
Alexandria, VA 22314

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(i), applicant(s) bring(s) to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed after payment of the issue fee. Applicant(s) respectfully request(s) that the Examiner place this Information Disclosure Statement and its accompanying PTO 1449 form in the application.

Copies of non-U.S. Patent documents are attached.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claims in the application and applicant(s) determine(s) that the cited document(s) do not constitute "prior art" under United States law, applicant(s) reserve(s) the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

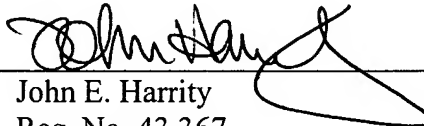
If any copending application(s) is/are cited on the attached PTO 1449, the Examiner's attention is directed to the foregoing application(s) in compliance with § 2001.06(b) of the Manual of Patent Examining Procedure. By identifying the copending application(s), the assignee and/or applicant of the application(s) do not waive confidentiality of the application(s). Accordingly, the U.S. Patent and Trademark Office is requested to maintain the confidentiality of the copending application(s) under 35 U.S.C. § 122.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By: _____


John E. Harrity
Reg. No. 43,367

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Suite 300
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INFORMATION DISCLOSURE CITATION PTO-1449	CUSTOMER NUMBER 45114	ATTORNEY'S DKT No. H1442	APPLICATION No. 10/728,910				
		APPLICANT(S) Wiley Eugene Hill et al.					
		FILING DATE December 8, 2003	GROUP 2818				
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	6,872,647	03/2005	Yu et al.	438	585		
	US 2004/0108545	06/2004	Y. Ando	257	329		
	US 2004/0188705	09/2004	Yeo et al.	257	170		
	US 2004/0227187	11/2004	Cheng et al.	257	347		
	6,855,588	02/2005	Liao et al.	438	197		
	US 2005/0001273	01/2005	Bryant et al.	257	369		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	JP 2004-281845	10/2004	Japan				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	J. I. Bergman et al.: "RTD/CMOS Nanoelectric Circuits: Thin-Film InP-Based Resonant Tunneling Diodes Integrated with CMOS Circuits," IEEE Electron Device Letters, Vol. 20, No. 3, March 1999, pages 119-122.						
	A. Seabaugh et al.: "Transistors and Tunnel Diodes For Analog/Mixed-Signal Circuits and Embedded Memory," IEDM, December 8, 1998, 4 pages.						
EXAMINER				DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).